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In re Application of: K. J. Barker, et al.

Serial Number: 09/834,591

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For: METHOD AND SYSTEM FOR FAST ETHERNET SERIAL PORT MULTIPLEXING TO REDUCE I/O PIN COUNT

Group Art Unit: 2661

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TRANSMITTAL OF FORMAL DRAWINGS

Attached please find formal drawings (3 sheets) for the above-identified application.

Respectfully submitted,

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